

FIG. 1A

PROCESSOR 110

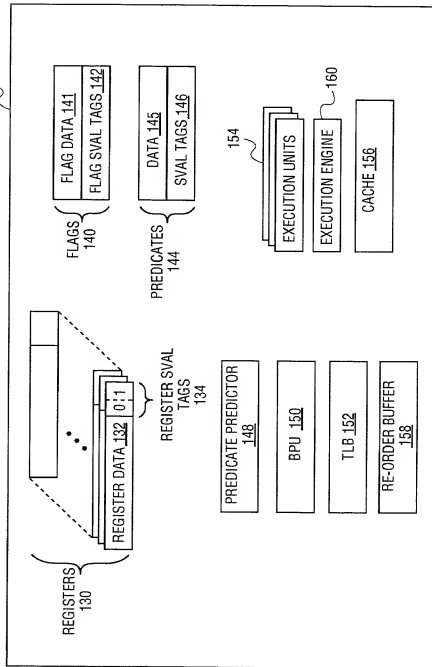


FIG. 1B

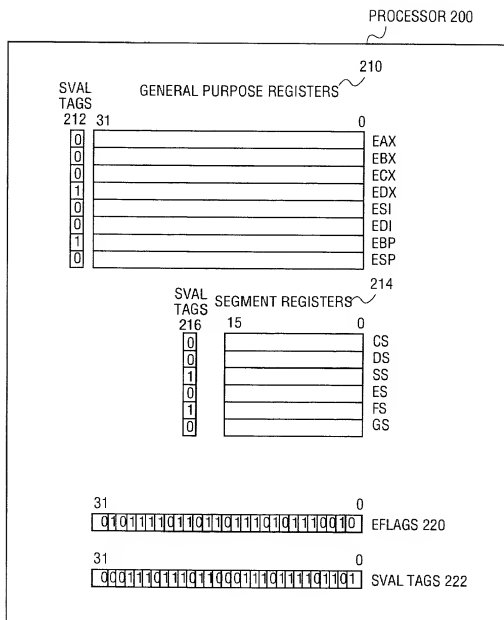


FIG. 2A

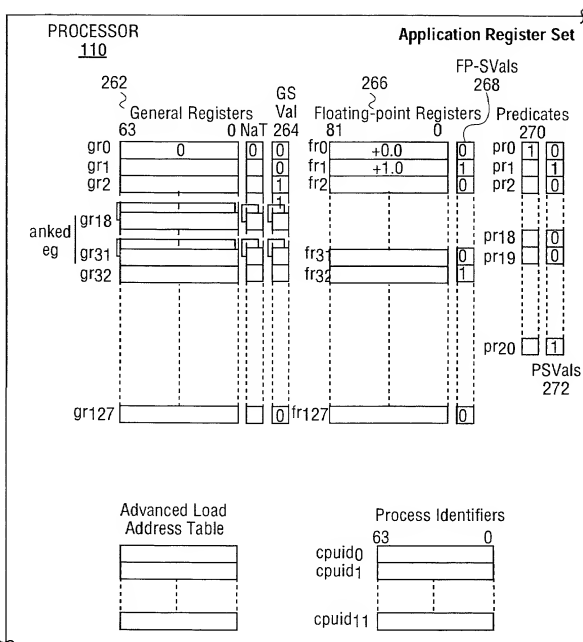


FIG. 2B-1

Application Register Set

PROCESSOR

110

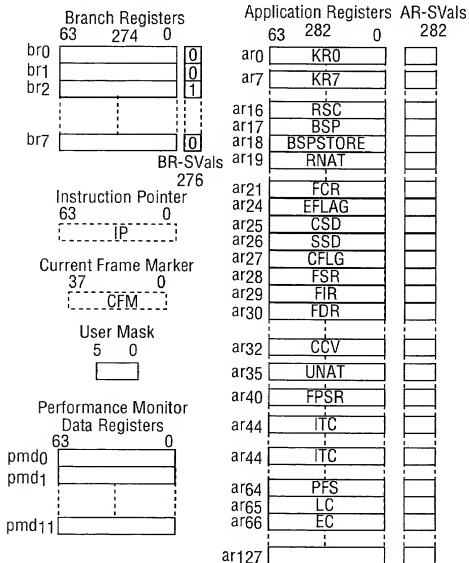


FIG. 2B-2

System Register Set

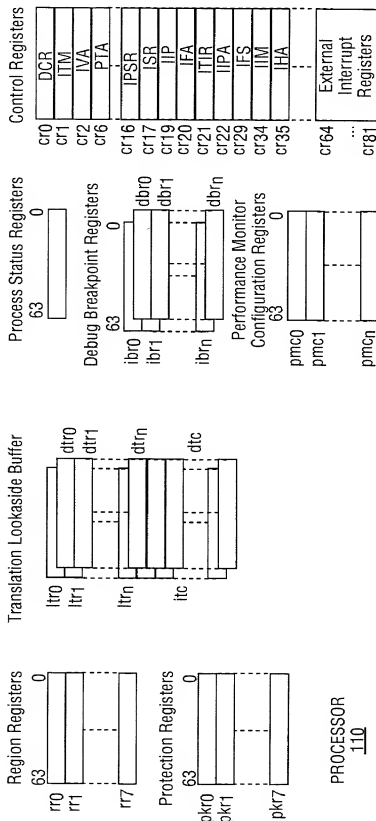


FIG. 2B-3

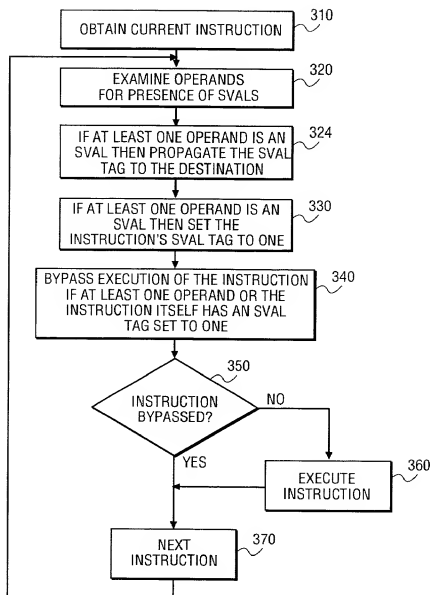


FIG. 3

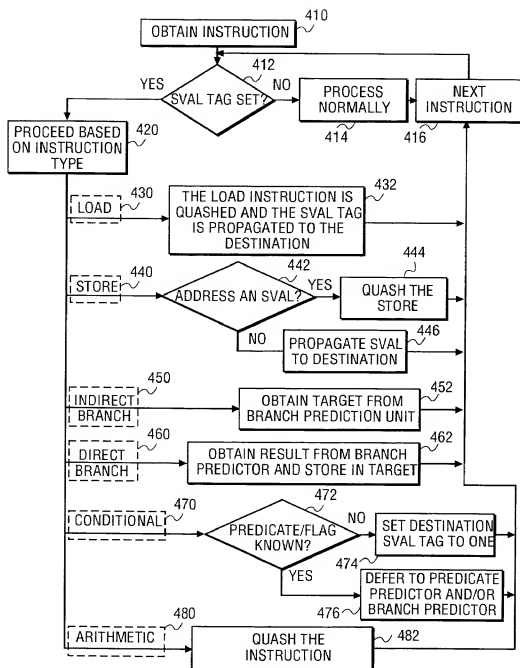


FIG. 4

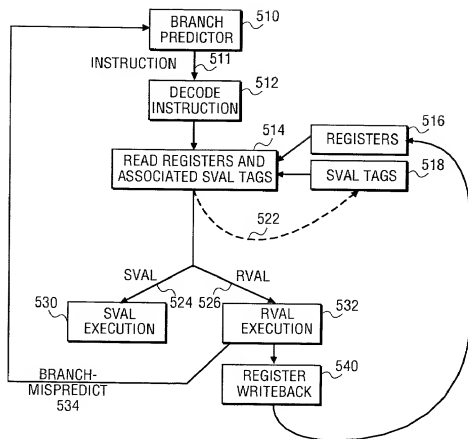


FIG. 5